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PAT-NO: JP405298204A

DOCUMENT-IDENTIFIER: JP 05298204 A

TITLE: TEST CIRCUT FOR INPUT/OUTPUT PROCESSOR

PUBN-DATE: November 12, 1993

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NAME

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APPL-NO: JP04121387

APPL-DATE: April 15, 1992

INT-CL (IPC): G06F013/00, G06F011/22, G06F013/12

## ABSTRACT:

PURPOSE: To improve the test operability to an input/output processor by  $% \left( 1\right) =\left( 1\right) \left( 1\right) +\left( 1\right) \left( 1\right) \left( 1\right) +\left( 1\right) \left( 1\right)$ 

specifying automatically a command against the occurrence of an error.

CONSTITUTION: A test executing part 2 gives an input/output operation

instruction to an input/output processor 4 by means of a channel program train

and sends the answer received from the processor 4 to the preceding instruction to an error detecting part 3 together with the expected value of the

answer. The part 3 compares the answer received from the processor 4 with the

The part 3 compares the answer received from the processor 4 with the expected

value of the answer. When the noncoincidence is secured between the answer and  $% \left( 1\right) =\left( 1\right) +\left( 1\right) +\left($ 

its expected value, a new channel program is produced. Thus an input/output

operation instructtion is given to the processor 4 based on the

channel program. Then the part 3 <u>compares</u> the answer given from the processor 4 to the input/output <u>operation</u> instruction with the <u>expected</u> value obtained through the pseudo execution of the channel <u>program</u>. Then the part 3 specifies a command against the occurrence of an error based on the result of the

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comparison.